

OCT. 29. 2000 9:41AM  
OCT. 27. 2000 9:01AM

CYPRESS SEMICONDUCTOR  
PATENT APPLICATION

NO. 446 NO. 481<sup>2</sup> P. 3  
KEY DOCKET NO. CY-00111

**DECLARATION AND POWER OF ATTORNEY  
IN PATENT APPLICATION**

I, below named inventor, I hereby declare that:  
residence/post office address and citizenship are as stated below next to my name;  
believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names  
listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:  
**ARCHITECTURE FOR EFFICIENT IMPLEMENTATION OF SERIAL DATA COMMUNICATION FUNCTIONS ON A  
PROGRAMMABLE LOGIC DEVICE (PLD)**

specification of which is attached hereto unless the following box is checked:  
( ) was filed on \_\_\_\_\_ as US Application Serial No. or PCT International Application  
Number \_\_\_\_\_ and was amended on \_\_\_\_\_ (if applicable).

I hereby state that I have reviewed and understood the contents of the above-identified specification, including the claims, as amended by  
any amendment(s) referred to above. I acknowledge the duty to disclose all information which is material to patentability as defined in 37  
CFR 1.556.

Foreign Application(s) and/or Claims of Foreign Priority  
I hereby claim foreign priority benefits under Title 35, United States Code Section 119 of any foreign application(s) for patent or inventor's certificate listed below and  
we also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application in which priority is claimed:

| COUNTRY | APPLICATION NUMBER | DATE FILED | PRIORITY CLAIMED UNDER 35 U.S.C. 119 |    |
|---------|--------------------|------------|--------------------------------------|----|
|         |                    |            | YES                                  | NO |
|         |                    |            |                                      |    |
|         |                    |            |                                      |    |

Revisional Application  
I hereby claim the benefit under Title 35, United States Code Section 119(e) of any United States provisional application(s) listed below:

| APPLICATION SERIAL NUMBER | FILED DATE |
|---------------------------|------------|
|                           |            |
|                           |            |

U.S. Priority Claims  
I hereby claim the benefit under Title 35, United States Code, Section 120 of any United States application(s) listed below and, insofar as the subject matter of each of the  
claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code Section 120, I  
acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, Section 1.56(a) which occurred between the filing date of the  
prior application and the national or PCT international filing date of this application:

| APPLICATION SERIAL NUMBER | FILED DATE | STATUS (patented/pending/abandoned) |
|---------------------------|------------|-------------------------------------|
|                           |            |                                     |
|                           |            |                                     |

**POWER OF ATTORNEY:**

As a named inventor, I hereby appoint the following attorney(s) and/or agent(s) listed below to prosecute this application and transact all business in the Patent and  
Trademark Office connected therewith.

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I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further  
that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001  
of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Full Name of First Inventor: Michael T. Moore

Residence: 8413 Benton Street, Santa Clara, CA USA 95051

Post Office Address: Same

Michael T. Moore  
Inventor's Signature

Date

10/29/00



OCT. 29. 2000 9:42AM

CYPRESS SEMICONDUCTOR

NO. 481 P. 4

PATENT APPLICATION

ATTORNEY DOCKET NO. CY-0016

DECLARATION AND POWER OF ATTORNEY  
FOR PATENT APPLICATION

Full Name of Second Inventor: James Lie

Citizenship: Hong Kong

Residence: 475 Dundee Avenue, Milpitas, CA USA 95035

Post Office Address: Same

Inventor's Signature

Date

10/29/00

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